

# Model-Checking Higher-Order Programs with Recursive Types

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**Abstract.** Model checking of higher-order recursion schemes (HORS, for short) has been recently studied as a new promising technique for automated verification of higher-order programs. The previous HORS model checking could however deal with only *simply-typed* programs, so that its application was limited to functional programs. To deal with a broader range of programs such as object-oriented programs and multi-threaded programs, we extend HORS model checking to check properties of programs with *recursive* types. Although the extended model checking problem is undecidable, we develop a sound model-checking algorithm that is relatively complete with respect to a recursive intersection type system and prove its correctness. Preliminary results on the implementation and applications to verification of object-oriented programs and multi-threaded programs are also reported.

## 1 Introduction

The model checking of higher-order recursion schemes (HORS for short) [15] has been recently studied as a new technique for automated verification of higher-order functional programs [9, 14, 16, 13]. HORS is essentially a simply-typed higher-order functional program with recursion for generating (possibly infinite) trees, and the goal of HORS model checking is to decide whether the tree generated by a given HORS satisfies a given property. The idea of applying the HORS model checking is to transform a given functional program  $M$  to a HORS  $\mathcal{G}$  that generates a tree describing possible outputs or event sequences of the program [9]; verification of the program is then reduced to HORS model checking, to decide whether the tree generated by  $\mathcal{G}$  represents valid outputs or event sequences. Based on this idea, various verification problems for functional programs have been reduced to it [9, 14, 16]. By combining it with predicate abstraction, a software model checker for functional programs can be constructed [16, 13].

The above approach to automated verification of functional programs, however, cannot be smoothly extended to support other important programming language features, such as objects and concurrency. Object-oriented programs often use (mutually) recursive interfaces, which cannot be naturally modeled by HORS (which are *simply-typed* functional programs). In fact, even Featherweight Java (FJ) [5] (with only objects as primitive data) is Turing complete [22]. As

for concurrency, the model checking of concurrent pushdown systems [20] is undecidable. These imply that there cannot be a sound and complete reduction from verification problems for object-oriented or recursive concurrent programs to HORS model checking. These situations are in sharp contrast to the case for functional programs, for which we have a sound and complete reduction to HORS model checking, as long as the programs use only finite base types (such as booleans, but not unbounded integers) [9].

The present paper aims to overcome the above limitations by introducing an extension of HORS model checking, where models, i.e., higher-order recursion schemes, are extended with recursive types. The extended higher-order recursion schemes, called  $\mu\text{HORS}$ , are essentially the simply-typed  $\lambda$ -calculus extended with tree constructors, (term-level) recursion, and recursive types, which is Turing complete. The model checking of  $\mu\text{HORS}$  ( $\mu\text{HORS}$  model checking for short) is undecidable, but we can develop a sound (but incomplete) model checking procedure. The procedure uses the result that HORS model checking can be reduced to a type checking problem in an intersection type system [9, 11, 24], and solves the type checking problem. Although the procedure is incomplete (as  $\mu\text{HORS}$  model checking is undecidable) and may not terminate, it is relatively complete with respect to a certain recursive intersection type system: any program that is typable in the type system is eventually proved correct. The procedure incorporates a novel reduction of the intersection type checking to SAT solving, which may be of independent interest and applicable to ordinary HORS checking.

Being armed with  $\mu\text{HORS}$  model checking, we can construct a fully automated verification tool (or so called a “software model checker”) for various programming languages. Given a program, we first apply a kind of program transformation to get a  $\mu\text{HORS}$  that generates a tree describing all the possible program behaviors of interest, and then use  $\mu\text{HORS}$  model checking to check that the tree describes only valid behaviors. As a proof of concept, we have implemented a prototype of the  $\mu\text{HORS}$  model checker and a translator from Featherweight Java (FJ) programs [5] to  $\mu\text{HORS}$ . Preliminary experiments show that we can indeed use the  $\mu\text{HORS}$  model checker to verify small but non-trivial object-oriented programs.

For the space restriction, we omit some examples and proofs, which are found in an extended version [10].

## 2 Preliminaries

This section introduces  $\mu\text{HORS}$ , defines model checking problems for them, and reduces it to a type-checking problem in a recursive intersection type system.

### 2.1 Recursive Intersection Types

Before introducing  $\mu\text{HORS}$  model checking, we first formalize recursive intersection types. We fix a finite set  $Q$  of base types below, and use the meta-variable  $q$  for its elements. We use the meta-variable  $\alpha$  for type variables.

**Definition 1.** A (recursive intersection) type is a pair  $(E, \alpha)$ , where  $E$  is a finite set of equations of the form  $\alpha_i = \sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$ , and  $\sigma$  is of the form  $\bigwedge\{\alpha_1, \dots, \alpha_k\}$ . Here  $m$  and  $k$  may be 0. We use the meta-variable  $\tau$  for recursive intersection types. We write  $\mathbf{Tv}(\tau)$  for the set of type variables occurring in  $\tau$ . A recursive intersection type  $\tau = (E, \alpha)$  is closed if, for every  $\alpha \in \mathbf{Tv}(\tau)$ ,  $(\alpha = \theta) \in E$  for some  $\theta$ . When  $(\alpha = \theta) \in E$ , we write  $E(\alpha)$  for  $\theta$ .

We identify types up to renaming of type variables. For example,  $(\{\alpha = q\}, \alpha)$  is the same as  $(\{\beta = q\}, \beta)$ . Thus, for two closed types  $\tau_0$  and  $\tau_1$ , we always assume that  $\mathbf{Tv}(\tau_0) \cap \mathbf{Tv}(\tau_1) = \emptyset$ . We often write  $\alpha_1 \wedge \dots \wedge \alpha_k$  or  $\bigwedge_{i \in \{1, \dots, k\}} \alpha_i$  for  $\bigwedge\{\alpha_1, \dots, \alpha_k\}$  and write  $\top$  for  $\bigwedge \emptyset$ . Intuitively,  $(E, \alpha)$  denotes the (recursive) type  $\alpha$  that satisfies the equations in  $E$ . For example,  $(\{\alpha = \alpha \rightarrow q\}, \alpha)$  represents the recursive type  $\mu\alpha.(\alpha \rightarrow q)$  in the usual notation. We often use this term notation for recursive intersection types. By abuse of notation, when  $E(\alpha) = \bigwedge_{i \in I_1} \alpha_i \rightarrow \dots \rightarrow \bigwedge_{i \in I_k} \alpha_i \rightarrow q$ , we write  $\bigwedge_{i \in I_1} (E, \alpha_i) \rightarrow \dots \rightarrow \bigwedge_{i \in I_k} (E, \alpha_i) \rightarrow q$  for  $(E, \alpha)$ . For example, when  $E = \{\alpha_0 = \alpha_1 \rightarrow q_0, \alpha_1 = q_1\}$ ,  $(E, \alpha_0)$  is also written as  $(E, \alpha_1) \rightarrow q_0$  or  $q_1 \rightarrow q_0$ . The type  $\sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$  describes functions that take  $m$  arguments of types  $\sigma_1, \dots, \sigma_m$ , and return a value of type  $q$ . The type  $\alpha_1 \wedge \dots \wedge \alpha_k$  describes values that have all of the types  $\alpha_1, \dots, \alpha_k$ . For example, if  $Q = \{q_1, q_2\}$ , the identity function on base values ( $\lambda x.x$  in the  $\lambda$ -calculus notation) would have types  $(q_1 \rightarrow q_1) \wedge (q_2 \rightarrow q_2)$ .

We define the subtyping relation  $\tau_0 \leq \tau_1$ , which intuitively means, as usual, that any value of type  $\tau_0$  can be used as a value of type  $\tau_1$ .

**Definition 2 (subtyping).** Let  $\tau = (E'', \alpha)$  and  $\tau' = (E', \alpha')$  be closed types, and let  $E = E'' \cup E'$ . The type  $\tau$  is a subtype of  $\tau'$ , written  $\tau \leq \tau'$ , if there exists a binary relation  $\mathcal{R}$  on  $\mathbf{Tv}(\tau) \cup \mathbf{Tv}(\tau')$  such that (i)  $(\alpha, \alpha') \in \mathcal{R}$  and (ii) for every  $(\alpha_0, \alpha'_0) \in \mathcal{R}$ , there exist  $\sigma_1, \dots, \sigma_m, \sigma'_1, \dots, \sigma'_m, q$  such that  $E(\alpha_0) = \sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$  and  $E(\alpha'_0) = \sigma'_1 \rightarrow \dots \rightarrow \sigma'_m \rightarrow q$ , with  $(\sigma'_1, \sigma_1), \dots, (\sigma'_m, \sigma_m) \in \mathcal{R}^\wedge$ . Here,  $\mathcal{R}^\wedge$  is:

$$\{(\bigwedge\{\alpha'_1, \dots, \alpha'_{k'}\}, \bigwedge\{\alpha_1, \dots, \alpha_k\}) \mid \forall i \in \{1, \dots, k\}. \exists j \in \{1, \dots, k'\}. \alpha'_j \mathcal{R} \alpha_i\}.$$

We write  $\tau \cong \tau'$  if  $\tau \leq \tau'$  and  $\tau' \leq \tau$ .

*Example 1.* Let  $\tau_0 = (\{\alpha_0 = \alpha_0 \rightarrow q\}, \alpha_0)$  and  $\tau_1 = (\{\alpha_1 = \alpha_2 \rightarrow q, \alpha_2 = \alpha_1 \wedge \alpha_3 \rightarrow q, \alpha_3 = q\}, \alpha_1)$ .  $\tau_1 \leq \tau_0$  holds, with the relation  $\{(\alpha_1, \alpha_0), (\alpha_0, \alpha_2)\}$  as a witness.

## 2.2 $\mu$ HORS

We introduce below  $\mu$ HORS and its model checking problem, and reduce the latter to a type checking problem. To our knowledge, the notion of  $\mu$ HORS is new, but it is a subclass of the untyped HORS studied by Tsukada and Kobayashi [24], and the reduction from  $\mu$ HORS model checking to type checking (Theorem 1) is a corollary of the result of [24]. We shall therefore quickly go through the definitions and results; more formal definitions (apart from recursive types) and intuitions are found in [15, 9, 24].

**$\mu$ HORS and model checking problems** The set of basic types (called *sorts*) is the subset of recursive intersection types, where  $Q$  is a singleton set  $\{\circ\}$  (where  $\circ$  is the type of trees) and there is no intersection: in  $\sigma = \bigwedge\{\alpha_1, \dots, \alpha_k\}$ ,  $k$  is always 1. Below we often use the following term representation of sorts:

$$\kappa ::= \alpha \mid \kappa_1 \rightarrow \dots \rightarrow \kappa_\ell \rightarrow \circ \mid \mu\alpha.\kappa.$$

Let  $\Sigma$  be a ranked alphabet, i.e., a map from symbols to their arities. An element of  $\text{dom}(\Sigma)$  is used as a tree constructor. A *sort environment* is a map from variables to sorts. The set of *applicative terms* of type  $\kappa$  under a sort environment  $\mathcal{K}$  is inductively defined by the following rules:

$$\frac{}{\mathcal{K}, x : \kappa \vdash x : \kappa} \qquad \frac{}{\mathcal{K} \vdash a : \underbrace{\circ \rightarrow \dots \rightarrow \circ}_{\Sigma(a)} \rightarrow \circ}$$

$$\frac{\mathcal{K} \vdash t_1 : \kappa_1 \quad \mathcal{K} \vdash t_2 : \kappa_2 \quad \kappa_1 \cong (\kappa_2 \rightarrow \kappa)}{\mathcal{K} \vdash t_1 t_2 : \kappa}$$

As usual, applications are left-associative, so that  $t_1 t_2 t_3$  means  $(t_1 t_2) t_3$ .

A  $\mu$ HORS  $\mathcal{G}$  is a quadruple  $(\mathcal{N}, \Sigma, \mathcal{R}, S)$  where: (i)  $\mathcal{N}$  is a map from variables (called *non-terminals*) to sorts; (ii)  $\Sigma$  is a ranked alphabet, where  $\text{dom}(\mathcal{N}) \cap \text{dom}(\Sigma) = \emptyset$ ; (iii)  $\mathcal{R}$  is a map from non-terminals to a  $\lambda$ -term of the form  $\lambda x_1. \dots \lambda x_\ell. t$  where  $t$  is an applicative term; (iv)  $S$ , called the *start symbol*, is a non-terminal such that  $\mathcal{N}(S) = \circ$ . If  $\mathcal{N}(F) = \kappa_1 \rightarrow \dots \rightarrow \kappa_k \rightarrow \circ$  and  $\mathcal{R}(F) = \lambda x_1. \dots \lambda x_\ell. t$ , then it must be the case that  $k = \ell$  and  $\mathcal{N}, x_1 : \kappa_1, \dots, x_\ell : \kappa_\ell \vdash t : \circ$ .

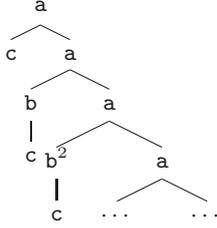
The (possibly infinite) tree generated by  $\mathcal{G}$ , written by  $\text{Tree}(\mathcal{G})$ , is defined as the limit of infinite fair reductions of  $S$  [15] where the reduction relation  $\longrightarrow$  is defined by: (i)  $F t_1 \dots t_\ell \longrightarrow [t_1/x_1, \dots, t_\ell/x_\ell]t$  if  $\mathcal{R}(F) = \lambda x_1. \dots \lambda x_\ell. t$ ; and (ii)  $a t_1 \dots t_\ell \longrightarrow a t_1 \dots t_{i-1} t'_i t_{i+1} \dots t_\ell$  if  $t_i \longrightarrow t'_i$  for some  $i \in \{1, \dots, \ell\}$ . See [15] for the formal definition of  $\text{Tree}(\mathcal{G})$ .

**Notation 1** We write  $\tilde{u}$  for a sequence  $u_1 \dots u_\ell$ .  $\lambda \tilde{x}. t$  stands for  $\lambda x_1. \dots \lambda x_\ell. t$ , and  $[\tilde{s}/\tilde{x}]t$  for  $[s_1/x_1, \dots, s_\ell/x_\ell]t$  (with the understanding that  $\tilde{s}$  and  $\tilde{x}$  have the same length  $\ell$ ). We often write the four components of  $\mathcal{G}$  as  $\mathcal{N}_{\mathcal{G}}, \Sigma_{\mathcal{G}}, \mathcal{R}_{\mathcal{G}}, S_{\mathcal{G}}$ , and omit the subscript if it is clear from context. We often write  $\mathcal{R}$  as a set of rewriting rules  $\{F_1 x_1 \dots x_{\ell_1} \rightarrow t_1, \dots, F_m x_1 \dots x_{\ell_m} \rightarrow t_m\}$  if  $\mathcal{R}(F_i) = \lambda x_1. \dots \lambda x_{\ell_i}. t_i$  for each  $i \in \{1, \dots, m\}$ .

*Example 2.* Consider  $\mu$ HORS  $\mathcal{G}_1 = (\mathcal{N}_1, \Sigma_1, \mathcal{R}_1, S)$  where  $\mathcal{N}_1 = \{S \mapsto \circ, F \mapsto (\circ \rightarrow \circ)\}$ ,  $\Sigma_1 = \{a \mapsto 2, b \mapsto 1, c \mapsto 0\}$ , and  $\mathcal{R}_1 = \{S \rightarrow F c, F x \rightarrow a x (F (b x))\}$ .  $S$  is rewritten as follows, and the tree in Figure 1 is generated:  $S \longrightarrow F c \longrightarrow a c (F (b c)) \longrightarrow a c (a (b c) (F (b (b c)))) \longrightarrow \dots$

*Example 3.* Consider  $\mu$ HORS  $\mathcal{G}_2 = (\mathcal{N}_2, \Sigma_1, \mathcal{R}_2, S)$  where  $\Sigma_1$  is as given in Example 2, and:  $\mathcal{N}_2 = \{S \mapsto \circ, F \mapsto (\circ \rightarrow \circ), G \mapsto \mu\alpha. (\alpha \rightarrow \circ \rightarrow \circ)\}$  and  $\mathcal{R}_2 = \{S \rightarrow F c, F x \rightarrow G G x, G g x \rightarrow a x (g g (b x))\}$ . This is the same as  $\mathcal{G}_1$  except that recursive types are used instead of term-level recursion.  $S$  is reduced as below, and the same tree as  $\text{Tree}(\mathcal{G}_1)$  is generated.

$$S \longrightarrow F c \longrightarrow G G c \longrightarrow a c (G G (b c)) \longrightarrow a c (a (b c) (G G (b (b c)))) \longrightarrow \dots$$



**Fig. 1.** The tree generated by  $\mathcal{G}_1$  of Example 2.

*Remark 1.* A tree node that is never instantiated to a terminal symbol is expressed by the special terminal symbol  $\perp$  (with arity 0). For example, for  $\mu\text{HORS}$   $\mathcal{G}_3 = (\mathcal{N}_3, \Sigma_1, \mathcal{R}_3, S)$  where  $\mathcal{N}_3 = \{S \mapsto \circ, F \mapsto \mu\alpha.(\alpha \rightarrow \circ)\}$  and  $\mathcal{R}_3 = \{S \rightarrow FF, Fx \rightarrow xx\}$ ,  $\text{Tree}(\mathcal{G}_3)$  is a singleton tree  $\perp$ .  $\square$

As usual [15, 9], we use (top-down) tree automata to express properties of the tree generated by higher-order recursion schemes. For a ranked alphabet  $\Sigma$ , a  $\Sigma$ -labeled tree  $T$  is a map from sequences of natural numbers (which represent paths of the tree) to  $\text{dom}(\Sigma)$ , such that (i) its domain  $\text{dom}(T)$  is non-empty and closed under the prefix operation, and (ii) if  $\pi \in \text{dom}(T)$  then  $\{j \mid \pi j \in \text{dom}(T)\} = \{1, \dots, \Sigma(T(\pi))\}$ . A (deterministic) *trivial automaton*  $\mathcal{B}$  is a quadruple  $(\Sigma, Q, \delta, q_0)$ , where  $\Sigma$  is a ranked alphabet,  $Q$  is a finite set of states,  $\delta$ , called a transition function, is a partial map from  $Q \times \text{dom}(\Sigma)$  to  $Q^*$  such that  $|\delta(q, a)| = \Sigma(a)$ , and  $q_0$  is the initial state. A  $\Sigma$ -labeled tree  $T$  is *accepted* by  $\mathcal{B}$  if there is a  $Q$ -labeled tree  $R$  (called a *run tree*) such that: (i)  $\text{dom}(T) = \text{dom}(R)$ ; (ii)  $R(\epsilon) = q_0$ ; and (iii) for every  $\pi \in \text{dom}(R)$ ,  $\delta(R(\pi), T(\pi)) = R(\pi 1) \dots R(\pi \Sigma(T(\pi)))$ . For a trivial automaton  $\mathcal{B} = (\Sigma, Q, \delta, q_0)$  (with  $\perp \notin \text{dom}(\Sigma)$ ), we write  $\mathcal{B}^\perp$  for the trivial automaton  $(\Sigma \cup \{\perp \mapsto 0\}, Q, \delta \cup \{(q, \perp) \mapsto \epsilon\} \mid q \in Q, q_0)$ . We often write  $\Sigma_{\mathcal{B}}, Q_{\mathcal{B}}, \delta_{\mathcal{B}}, q_{\mathcal{B}, 0}$  for the four components of  $\mathcal{B}$ , and omit the subscript if it is clear from context. Trivial automata are sufficient for describing safety properties: see [12] for the logical characterization.

*Example 4.* Let  $\mathcal{B}_1 = (\Sigma_1, \{q_0, q_1\}, \delta, q_0)$  where  $\Sigma_1$  is as given in Example 2 and  $\delta$  is given by:  $\delta(q_0, \mathbf{a}) = q_0 q_0$ ,  $\delta(q_0, \mathbf{b}) = \delta(q_1, \mathbf{b}) = q_1$ , and  $\delta(q_0, \mathbf{c}) = \delta(q_1, \mathbf{c}) = \epsilon$ . It accepts a  $\Sigma_1$ -labeled (ranked) tree  $T$  if and only if  $\mathbf{a}$  does not occur below  $\mathbf{b}$ . In particular,  $\mathcal{B}_1$  accepts the tree shown in Figure 1.  $\square$

The  $\mu\text{HORS}$  *model checking* is the problem of checking whether  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^\perp$ , given a  $\mu\text{HORS}$   $\mathcal{G}$  and a trivial automaton  $\mathcal{B}$ . The problem is in general undecidable [24]. We give a sound type system for checking that  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^\perp$ . The set of recursive intersection types is as given in Section 2.1, where the set  $Q$  of base types is the set of states of  $\mathcal{B}$ . Intuitively, a state  $q$  is regarded as the type of trees accepted by  $\mathcal{B}^\perp$  from the state  $q$  [9].

The type judgment relations  $\Gamma \vdash_{\mathcal{B}} t : \tau$  and  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, t) : \tau$  (where  $\Gamma$ , called a type environment, is a set of type bindings of the form  $x : \tau$ ) are defined by:

$$\begin{array}{c}
\frac{\tau \leq \tau'}{\Gamma, x : \tau \vdash_{\mathcal{B}} x : \tau'} \qquad \frac{\delta_{\mathcal{B}}(q, a) = q_1 \cdots q_k \quad q_1 \rightarrow \cdots \rightarrow q_k \rightarrow q \leq \tau}{\Gamma \vdash_{\mathcal{B}} a : \tau} \\
\\
\frac{\Gamma \vdash_{\mathcal{B}} t_1 : \bigwedge_{i \in I} \tau_i \rightarrow \tau \quad \Gamma \vdash_{\mathcal{B}} t_2 : \tau'_i \text{ and } \tau'_i \leq \tau_i \text{ (for every } i \in I)}{\Gamma \vdash_{\mathcal{B}} t_1 t_2 : \tau} \qquad \frac{\Gamma, x : \tau_1, \dots, x : \tau_\ell \vdash_{\mathcal{B}} t : \tau \quad x \text{ does not occur in } \Gamma}{\Gamma \vdash_{\mathcal{B}} \lambda x. t : \bigwedge_{i \in \{1, \dots, \ell\}} \tau_i \rightarrow \tau} \\
\\
\frac{\forall (F : \tau) \in \Gamma. (\Gamma \vdash_{\mathcal{B}} \mathcal{R}(F) : \tau)}{\vdash_{\mathcal{B}} \mathcal{R} : \Gamma} \qquad \frac{\vdash_{\mathcal{B}} \mathcal{R}_{\mathcal{G}} : \Gamma \quad \Gamma \vdash_{\mathcal{B}} t : \tau}{\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, t) : \tau}
\end{array}$$

The following theorem is a special case of the soundness of Tsukada and Kobayashi's infinite intersection type system for untyped HORS [24].

**Theorem 1 (soundness).** *Let  $\mathcal{B}$  be a trivial automaton  $(\Sigma, Q, \delta, q_{\mathcal{B},0})$  and  $\mathcal{G}$  be a  $\mu$ HORS. If  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$ , then  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^{\perp}$ .*

*Example 5.* Recall  $\mathcal{G}_1$  and  $\mathcal{G}_2$  in Examples 2 and 3, and  $\mathcal{B}_1$  in Example 4.  $\Gamma_1 \vdash_{\mathcal{B}_1} (\mathcal{G}_1, S) : q_0$  and  $\Gamma_2 \vdash_{\mathcal{B}_1} (\mathcal{G}_2, S) : q_0$  hold for  $\Gamma_1 = \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0\}$  and  $\Gamma_2 = \Gamma_1 \cup \{G : \mu\alpha. (\alpha \rightarrow (q_0 \wedge q_1) \rightarrow q_0)\}$ .  $\square$

Given a type environment  $\Gamma$ , a  $\mu$ HORS  $\mathcal{G}$ , and an automaton  $\mathcal{B}$ , it is decidable whether  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds. Thus,  $\Gamma$  can be used as a certificate for  $\text{Tree}(\mathcal{G})$  being accepted by  $\mathcal{B}$ . The converse of the theorem above does not hold, i.e., there is a  $\mu$ HORS  $\mathcal{G}$  such that  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^{\perp}$  but  $\text{Tree}(\mathcal{G})$  is not well-typed. We have the following properties on the (un)decidability of type checking. See [10] for a proof.

**Theorem 2.** *1. Given a type environment  $\Gamma$ , a  $\mu$ HORS  $\mathcal{G}$ , and a trivial automaton  $\mathcal{B}$ , it is decidable whether  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds.*  
*2. Given a  $\mu$ HORS  $\mathcal{G}$  and a trivial automaton  $\mathcal{B}$ , it is undecidable whether there exists  $\Gamma$  such that  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds.*

### 3 Model Checking $\mu$ HORS

We now describe the main result of this paper: a model checking procedure for  $\mu$ HORS. We shall develop a procedure CHECK that satisfies:

$$\text{CHECK}(\mathcal{G}, \mathcal{B}) = \begin{cases} \Gamma' \text{ such that } \Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0} \text{ if } \exists \Gamma. \Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0} \\ \text{No (with a counterexample) if } \text{Tree}(\mathcal{G}) \text{ is not accepted by } \mathcal{B}^{\perp} \end{cases}$$

By Theorem 2, the procedure CHECK can only be a semi-algorithm: it may not terminate if  $\text{Tree}(\mathcal{G})$  is accepted by  $\mathcal{B}^{\perp}$  but  $\exists \Gamma. \Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  does not hold.

An obvious approach would be to run (i) a sub-procedure FINDCERT( $\mathcal{G}, \mathcal{B}$ ) to enumerate all the finite type environments  $\Gamma$  and output  $\Gamma$  if  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds, and in parallel, (ii) a sub-procedure FINDCE( $\mathcal{G}, \mathcal{B}$ ) to reduce  $\mathcal{G}$  in a fair manner and output No if a partially generated tree is not accepted by  $\mathcal{B}^{\perp}$ . The first sub-procedure FINDCERT is, however, too non-deterministic to be used in practice.

We describe below a more realistic procedure for  $\text{FINDCERT}(\mathcal{G}, \mathcal{B})$  that outputs  $\Gamma$  such that  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  if there is any, and may diverge otherwise. As  $\text{FINDCERT}$  can incrementally find the types of non-terminals, we can use them to improve  $\text{FINDCE}$  as well, by removing well-typed terms from the search space. As such interaction between  $\text{FINDCERT}$  and  $\text{FINDCE}$  is the same as the case without recursive types [8], we focus on the discussion of  $\text{FINDCERT}$  below.

### 3.1 Type Inference Procedure

We first give an informal overview of the idea of  $\text{FINDCERT}$ . Since it is easy to check whether a given  $\Gamma$  is a valid certificate (i.e. whether  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$  holds), the main issue is how to find candidates for  $\Gamma$ . As in the algorithm for HORS without recursive types [8], the idea of finding  $\Gamma$  is to extract type information by partially reducing a given recursion scheme, and observing how each non-terminal symbol is used. For example, suppose that  $S$  is reduced as follows.  $S : q_0 \longrightarrow^* C_1[F G : q_1] \longrightarrow^* C_2[G t : q_2] \longrightarrow^* C_3[t : q_1]$ . Here, we have annotated each term with a state of the property automaton;  $t : q$  means that the tree generated by  $t$  should be accepted from  $q$ . From the reduction sequence, we know  $t$  should have type  $q_1$ , from which we can guess that  $G$  should have type  $q_1 \rightarrow q_2$ , and we can further guess that  $F$  should have type  $(q_1 \rightarrow q_2) \rightarrow q_1$ . This way of guessing types is complete for HORS (without recursive types) [8]. In the presence of recursive types, however, we need a further twist, to obtain (relative) completeness. For example, suppose  $S$  is reduced as follows.  $S : q_0 \longrightarrow^* C_1[F t_1 : q_1] \longrightarrow^* C_2[t_1 t_2 : q_0] \longrightarrow^* C_3[t_2 t_3 : q_1] \longrightarrow^* C_4[t_3 t_4 : q_0] \longrightarrow^* \dots$ . This kind of calling chain terminates for ordinary HORS (since the terms are simply-typed), but may not terminate for  $\mu\text{HORS}$  because of recursive types. (For example, consider a variation of  $\mathcal{G}_3$  in Remark 1, where the rule for  $F$  is replaced by  $F x \rightarrow x (I x)$ , with the new rule  $I x \rightarrow x$ . Then, we have an infinite calling chain:  $S \longrightarrow^* F (I F) \longrightarrow^* (I F) (I (I F)) \longrightarrow^* (I (I F)) (I (I (I F))) \longrightarrow^* \dots$ ). Thus, we would obtain an infinite set of type equations:

$$\alpha_F = \alpha_{t_1} \rightarrow q_1 \quad \alpha_{t_1} = \alpha_{t_2} \rightarrow q_0 \quad \alpha_{t_2} = \alpha_{t_3} \rightarrow q_1 \quad \alpha_{t_3} = \alpha_{t_4} \rightarrow q_0 \quad \dots$$

(where  $\alpha_t$  represents the type of term  $t$ ). To address this problem, we introduce an equivalence relation  $\sim$  on terms, and consider reductions modulo  $\sim$ . In the example above, if we choose  $\sim$  so that  $t_{2n-1} \sim t_{2n+1}$  and  $F \sim t_{2n} \sim t_{2n+2}$ , then we would have finite equations  $\alpha_{[F]} = \alpha_{[t_1]} \rightarrow q_1$  and  $\alpha_{[t_1]} = \alpha_{[F]} \rightarrow q_0$  (where  $[t]$  is the equivalence class containing  $t$ ), from which we can infer  $\mu\alpha.(\alpha \rightarrow q_0) \rightarrow q_1$  as the type of  $F$ . As we show in Theorem 3 later, this way of type inference is complete *if a proper equivalence relation  $\sim$  is given as an oracle*. It is not complete in general, but Theorem 4 ensures that no matter how  $\sim$  is chosen, we can “amend” the inferred type environment to obtain a correct type environment. Based on the theorem, we can develop a complete procedure for  $\text{FINDCERT}$ .

We now turn to describe the idea more formally. Let  $\mathbf{Tm}$  be the set of (well-sorted) closed terms constructed from non-terminals and terminals of  $\mathcal{G}$ , and  $\sim$  be an equivalence relation on  $\mathbf{Tm}$  that induces a *finite* set of equivalence classes. We write  $[t]_{\sim}$  for the equivalence class containing  $t$ , i.e.,  $\{t' \mid t \sim t'\}$ , and omit the subscript if clear from context. Intuitively, the equivalence relation  $t_1 \sim t_2$



represent reductions by using rule R-EQ, and solid arrows represent reductions obtained by the other rules. From an infinite fair reduction sequence, we obtain the following set as  $\mathcal{U}$ :

$$\{(F(\mathbf{b}^k \mathbf{c}), q_0), (GG(\mathbf{b}^k \mathbf{c}), q_0), (\mathbf{b}^k \mathbf{c}, q_0), (\mathbf{b}^k \mathbf{c}, q_1) \mid k \geq 0\} \\ \cup \{(S, q_0)\} \cup \{(\mathbf{a}(\mathbf{b}^k \mathbf{c})(GG(\mathbf{b}^\ell \mathbf{c})), q_0) \mid k, \ell \geq 0\} \quad \square$$

The goal below is to construct a candidate of type environment  $\Gamma$  that satisfies  $\Gamma \vdash_{\mathcal{B}} \mathcal{G} : q_0$ , from a fair reduction sequence (where a reduction sequence is fair if every enabled reduction is eventually reduced). The idea of the construction of  $\Gamma$  is similar to the case for ordinary HORS [8]. For example, in Example 6 above, from the pairs  $(\mathbf{c}, q_0)$  and  $(\mathbf{c}, q_1)$ , we can guess that the type of  $\mathbf{c}$  is  $q_0 \wedge q_1$ . From the pair  $(F \mathbf{c}, q_0)$ , we guess that the return type of  $F$  is  $q_0$ , so that the type of  $F$  is  $q_0 \wedge q_1 \rightarrow q_0$ . The actual construction is, however, more involved than [8] because of the presence of recursive types and the term equivalence relation  $\sim$ .

Let  $(\mathcal{X}_0, \mathcal{U}_0) \rightarrow_{\sim} (\mathcal{X}_1, \mathcal{U}_1) \rightarrow_{\sim} \dots$  be a fair reduction sequence where  $\mathcal{X}_0 = \{S\}$  and  $\mathcal{U}_0 = \{(S, q_0)\}$ , and let  $\mathcal{X}$  and  $\mathcal{U}$  be  $\bigcup_{i \in \omega} \mathcal{X}_i$  and  $\bigcup_{i \in \omega} \mathcal{U}_i$  respectively. We prepare a type variable  $\alpha_{[t_0], \dots, [t_k], q}$  for each  $(t_0 t_1 \dots t_k, q) \in \mathcal{U}$ . Intuitively,  $\alpha_{[t_0], \dots, [t_k], q}$  is the type of  $t_0$  in  $t_0 t_1 \dots t_k : q$ . Let  $E$  be:

$$\{\alpha_{[t_0], [t_1], \dots, [t_k], q} = \sigma_{[t_1]} \rightarrow \dots \rightarrow \sigma_{[t_k]} \rightarrow q \mid (t_0 t_1 \dots t_k, q) \in \mathcal{U}\},$$

where  $\sigma_{[t]} = \bigwedge \{\alpha_{[t], [t'_1], \dots, [t'_\ell], q'} \mid (t t'_1 \dots t'_\ell, q') \in \mathcal{U}\}$ . We define the type environment  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  as  $\{F : (E, \alpha_{[F], [t_1], \dots, [t_k], q}) \mid (F t_1 \dots t_k, q) \in \mathcal{U}\}$ . By the condition that  $\sim$  induces a *finite* number of equivalence classes,  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  is finite.

*Example 7.* From the reductions in Example 6, we get the following type equations:

$$\alpha_{S, q_0} = q_0 \quad \alpha_{F, \mathbf{c}, q_0} = \alpha_{\mathbf{c}, q_0} \wedge \alpha_{\mathbf{c}, q_1} \rightarrow q_0 \quad \alpha_{\mathbf{c}, q_0} = q_0 \quad \alpha_{\mathbf{c}, q_1} = q_1 \\ \alpha_{G, G, \mathbf{c}, q_0} = \alpha_{G, G, \mathbf{c}, q_0} \rightarrow \alpha_{\mathbf{c}, q_0} \wedge \alpha_{\mathbf{c}, q_1} \rightarrow q_0$$

Thus, the extracted type environment (in the usual term representation) is:

$$\{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \mu \alpha. (\alpha \rightarrow (q_0 \wedge q_1) \rightarrow q_0)\}. \quad \square$$

The theorem below (see [10] for a proof) ensures that if  $\mathcal{G}$  is typable and if  $\sim$  is properly chosen,  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  is a proper witness. For a type environment  $\Gamma$ , we define the equivalence relation  $\sim_{\Gamma}$  by:  $\sim_{\Gamma} = \{(t_1, t_2) \mid \forall \tau. (\Gamma \vdash t_1 : \tau \iff \Gamma \vdash t_2 : \tau)\}$ .

**Theorem 3.** *If  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B}, 0}$  and  $\sim \subseteq \sim_{\Gamma}$ , then  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B}, 0}$ .*

*Example 8.* Recall  $\mathcal{G}_2$  in Example 3, and  $\Gamma_2 = \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \mu \alpha. (\alpha \rightarrow (q_0 \wedge q_1) \rightarrow q_0)\}$  in Example 5. The relation  $\sim$  in Example 6 satisfies the assumption  $\sim \subseteq \sim_{\Gamma_2}$  of Theorem 3, and  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} \vdash_{\mathcal{B}_1} (\mathcal{G}_2, S) : q_0$  holds indeed.

Theorem 3 cannot be directly used for type inference, since we do not know  $\sim_{\Gamma}$  in advance. We shall prove below (in Theorem 4) that even if  $\sim$  is not a subset of  $\sim_{\Gamma}$ , we can “amend” the type environment to get a valid one, by using the refinement relation  $\sqsubseteq$  below. Intuitively,  $\tau_1 \sqsubseteq \tau_2$  means that  $\tau_1$  is obtained from  $\tau_2$  by removing some intersection types. Note that unlike subtyping, the refinement relation is co-variant in the function type constructor ( $\rightarrow$ ).

**Definition 3 (refinement).** Let  $\tau_0 = (E_0, \alpha_0)$  and  $\tau_1 = (E_1, \alpha_1)$  be closed types, and let  $E = E_0 \cup E_1$ . The type  $\tau_0$  is a refinement of  $\tau_1$ , written  $\tau_0 \sqsubseteq \tau_1$ , if there exists a binary relation  $\mathcal{R}$  on  $\mathbf{Tv}(\tau_1) \cup \mathbf{Tv}(\tau_2)$  such that (i)  $(\tau_0, \tau_1) \in \mathcal{R}$  and (ii) for every  $(\tau'_0, \tau'_1) \in \mathcal{R}$ , there exist  $\sigma_1, \dots, \sigma_m, \sigma'_1, \dots, \sigma'_m, q$  such that  $E(\alpha'_0) = \sigma_1 \rightarrow \dots \rightarrow \sigma_m \rightarrow q$  and  $E(\alpha'_1) = \sigma'_1 \rightarrow \dots \rightarrow \sigma'_m \rightarrow q$ , with  $(\sigma_1, \sigma'_1), \dots, (\sigma_m, \sigma'_m) \in \mathcal{R}^\sqsubseteq$ . Here,  $\mathcal{R}^\sqsubseteq$  is defined as:  
 $\{(\wedge\{\alpha_1, \dots, \alpha_k\}, \wedge\{\alpha'_1, \dots, \alpha'_{k'}\}) \mid \forall i \in \{1, \dots, k\}. \exists j \in \{1, \dots, k'\}. \alpha_i \mathcal{R} \alpha'_j\}$ .

We write  $\Gamma_1 \sqsubseteq \Gamma_2$  if  $\text{dom}(\Gamma_1) \subseteq \text{dom}(\Gamma_2)$  and for every  $x : \tau_1 \in \Gamma_1$ , there exists  $\tau_2$  such that  $x : \tau_2 \in \Gamma_2$  and  $\tau_1 \sqsubseteq \tau_2$ .

*Example 9.* Let  $\tau_1$  be  $q_1 \rightarrow q_2$  and  $\tau_2$  be  $(q_1 \wedge q_0) \rightarrow q_2$ . Then  $\tau_1 \sqsubseteq \tau_2$  and  $\tau_1 \rightarrow q_0 \sqsubseteq \tau_2 \rightarrow q_0$  hold. Note that  $\tau_1 \leq \tau_2$  but  $\tau_1 \rightarrow q_0 \not\leq \tau_2 \rightarrow q_0$ .  $\square$

**Theorem 4.** Suppose  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S_{\mathcal{G}}) : q_{\mathcal{B},0}$ . Let  $\sim$  be an equivalence relation on  $\mathbf{Tm}$  and  $(\mathcal{X}_0, \mathcal{U}_0) \rightarrow_{\sim} (\mathcal{X}_1, \mathcal{U}_1) \rightarrow_{\sim} (\mathcal{X}_2, \mathcal{U}_2) \rightarrow_{\sim} \dots$  be a fair reduction sequence, with  $(\mathcal{X}_0, \mathcal{U}_0) = (\{S\}, \{(S, q_{\mathcal{B},0})\})$ . Let  $\mathcal{U} = \bigcup_i \mathcal{U}_i$  and  $\mathcal{X} = \bigcup_i \mathcal{X}_i$ . Then, there exists  $\Gamma'$  such that  $\Gamma' \sqsubseteq \Gamma_{\mathcal{X}, \mathcal{U}, \sim}$  and  $\Gamma' \vdash (\mathcal{G}, S) : q_{\mathcal{B},0}$ .

The proof is given in the extended version [10]. Intuitively, Theorem 4 holds because, if  $\sim$  is not a subset of  $\sim_{\Gamma}$ , we only get extra reduction sequences, whose effect is only to add extra type bindings and elements in intersections. Thus, by removing the extra nodes and edges (using the refinement relation from right to left), we can obtain a proper type environment.

*Example 10.* Recall Example 6. Let  $\sim^{(2)}$  be  $\sim^{(1)} \cup \{(GG, \mathbf{b}), (\mathbf{b}, GG)\}$ . In addition to the reductions in Example 6, we obtain the extra reduction sequence:  $(\mathbf{b} \mathbf{c}, q_1) \rightarrow (GG \mathbf{c}, q_1) \rightarrow (\mathbf{a} \mathbf{c} (GG (\mathbf{b} \mathbf{c})), q_1) \rightarrow \mathbf{fail}$ . From the reductions, we obtain the following type equations:

$$\begin{aligned} \alpha_{S, q_0} &= q_0 & \alpha_{F, \mathbf{c}, q_0} &= \alpha_{\mathbf{c}, q_0} \wedge \alpha_{\mathbf{c}, q_1} \rightarrow q_0 & \alpha_{\mathbf{c}, q_0} &= q_0 & \alpha_{\mathbf{c}, q_1} &= q_1 \\ \alpha_{G, G, \mathbf{c}, q_0} &= \alpha_{G, G, \mathbf{c}, q_0} \wedge \alpha_{G, G, \mathbf{c}, q_1} \rightarrow \alpha_{\mathbf{c}, q_0} \wedge \alpha_{\mathbf{c}, q_1} \rightarrow q_0 \\ \alpha_{G, G, \mathbf{c}, q_1} &= \alpha_{G, G, \mathbf{c}, q_1} \wedge \alpha_{G, G, \mathbf{c}, q_1} \rightarrow \alpha_{\mathbf{c}, q_0} \wedge \alpha_{\mathbf{c}, q_1} \rightarrow q_1 \end{aligned}$$

The part obtained from the extra reduction sequence is underlined. By ignoring that part, we get the same equations as Example 7, hence obtaining the correct type environment:  $\{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \mu\alpha. (\alpha \rightarrow (q_0 \wedge q_1) \rightarrow q_0)\}$ .  $\square$

Theorem 4 yields the procedure `FINDCERT` in Figure 2. The condition  $\exists \Gamma'. \Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0} \wedge \Gamma' \sqsubseteq \Gamma$  is in general undecidable because of the presence of recursive types. Thus, we bound the size (i.e., the number of type constructors) of  $\Gamma'$  by  $v$ , and gradually increase the bound. An algorithm to check whether there exists  $\Gamma'$  such that  $|\Gamma'| < v$  and  $\Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0} \wedge \Gamma' \sqsubseteq \Gamma$  is discussed in Section 3.2. By Theorem 4, we have:

**Theorem 5 (relative completeness).** If  $\Gamma \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  for some finite recursive type environment  $\Gamma$ , then `FINDCERT` $(\mathcal{G}, S, q_{\mathcal{B},0})$  eventually terminates and outputs  $\Gamma'$  such that  $\Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$ .

To see the termination, notice that by the condition that  $\sim$  induces a finite number of equivalence classes, there exists  $m$  such that  $\Gamma_{\mathcal{X}, \mathcal{U}, \sim} = \Gamma_{\mathcal{X}_m, \mathcal{U}_m, \sim}$  in Theorem 4.

```

FINDCERT( $\mathcal{G}, \mathcal{B}$ ) = Rep( $\mathcal{G}, \mathcal{B}, \{S\}, \{(S, q_{\mathcal{B},0})\}, \{(S, S)\}, 1$ )
Rep( $\mathcal{G}, \mathcal{B}, \mathcal{X}, \mathcal{U}, \sim, v$ ) =
  let ( $\mathcal{X}, \mathcal{U}$ )  $\xrightarrow{\ell}$  ( $\mathcal{X}', \mathcal{U}'$ ) in let  $\sim' = \text{expandEq}(\sim, \mathcal{X}')$  in
    if  $\Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  for some  $\Gamma' \sqsubseteq \Gamma_{\mathcal{X}', \mathcal{U}', \sim'}$  and  $|\Gamma'| \leq v$  then return  $\Gamma'$ 
    else Rep( $\mathcal{G}, \mathcal{B}, \mathcal{X}', \mathcal{U}', \sim', v+1$ )

```

**Fig. 2.** A type inference procedure. ( $|\Gamma|$  denotes the largest type size in  $\Gamma$ .)

### 3.2 Type Checking by SAT Solving

We now discuss the sub-algorithm for FINDCERT, to check whether there exists  $\Gamma'$  such that  $|\Gamma'| \leq v$  and  $\Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0} \wedge \Gamma' \sqsubseteq \Gamma$ .

We first rephrase the condition  $|\Gamma'| \leq v \wedge \Gamma' \sqsubseteq \Gamma$ . For a set  $E = \{\alpha_1 = \sigma_{1,1} \rightarrow \dots \rightarrow \sigma_{1,m_1} \rightarrow q_1, \dots, \alpha_n = \sigma_{n,1} \rightarrow \dots \rightarrow \sigma_{n,m_n} \rightarrow q_n\}$ , we write  $E^{(k)}$  for:

$$\{\alpha_1^{(1)} = \sigma_{1,1}^{(k)} \rightarrow \dots \rightarrow \sigma_{1,m_1}^{(k)} \rightarrow q_1, \dots, \alpha_n^{(1)} = \sigma_{n,1}^{(k)} \rightarrow \dots \rightarrow \sigma_{n,m_n}^{(k)} \rightarrow q_n, \dots, \\ \alpha_1^{(k)} = \sigma_{1,1}^{(k)} \rightarrow \dots \rightarrow \sigma_{1,m_1}^{(k)} \rightarrow q_1, \dots, \alpha_n^{(k)} = \sigma_{n,1}^{(k)} \rightarrow \dots \rightarrow \sigma_{n,m_n}^{(k)} \rightarrow q_n, \}$$

obtained by preparing  $k$  copies for each type variable. Here, for  $\sigma = \bigwedge \{\alpha_1, \dots, \alpha_\ell\}$ ,  $\sigma^{(k)}$  represents  $\bigwedge \{\alpha_1^{(1)}, \dots, \alpha_\ell^{(1)}, \dots, \alpha_1^{(k)}, \dots, \alpha_\ell^{(k)}\}$ . Clearly,  $(E, \alpha_i) \cong (E^{(k)}, \alpha_i^{(1)})$ . We write  $\Gamma^{(k)}$  for  $\{x : (E^{(k)}, \alpha^{(i)}) \mid x : (E, \alpha) \in \Gamma, 1 \leq i \leq k\}$ .

We write  $E \sqsubseteq_{\mathcal{S}} E'$  if  $E$  is obtained from  $E'$  by removing some elements from intersections, i.e., if  $E = \{\alpha_1 = \bigwedge S_{1,1} \rightarrow \dots \rightarrow \bigwedge S_{1,m_1} \rightarrow q_1, \dots, \alpha_n = \bigwedge S_{n,1} \rightarrow \dots \rightarrow \bigwedge S_{n,m_n} \rightarrow q_n\}$  and  $E' = \{\alpha_1 = \bigwedge S'_{1,1} \rightarrow \dots \rightarrow \bigwedge S'_{1,m_1} \rightarrow q_1, \dots, \alpha_n = \bigwedge S'_{n,1} \rightarrow \dots \rightarrow \bigwedge S'_{n,m_n} \rightarrow q_n\}$  with  $S_{i,j} \subseteq S'_{i,j}$  for every  $i, j$ . It is pointwise extended to  $\Gamma \sqsubseteq_{\mathcal{S}} \Gamma'$  by:  $\Gamma \sqsubseteq_{\mathcal{S}} \Gamma' \iff \forall x : (E, \alpha) \in \Gamma, \exists x : (E', \alpha) \in \Gamma'. E \sqsubseteq_{\mathcal{S}} E'$ . Then,  $\Gamma' \sqsubseteq \Gamma$  is equivalent to  $\exists k. \Gamma' \sqsubseteq_{\mathcal{S}} \Gamma^{(k)}$  (up to renaming of type variables). Thus, the condition  $|\Gamma'| \leq v \wedge \Gamma' \sqsubseteq \Gamma$  in the algorithm can be replaced by  $\Gamma' \sqsubseteq_{\mathcal{S}} \Gamma^{(v)}$  without losing completeness.

To check whether there exists  $\Gamma'$  such that  $\Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  and  $\Gamma' \sqsubseteq_{\mathcal{S}} \Gamma^{(k)}$ , we attach a boolean variable to each type binding and each element of an intersection in  $\Gamma^{(k)}$ , to express whether  $\Gamma'$  has the corresponding binding or element. Thus, an annotated type environment is of the form  $\{x_1 :^{b_1} \tau_1, \dots, x_m :^{b_m} \tau_m\}$ , where each type equation in  $\tau_1, \dots, \tau_m$  is now of the form:

$$\alpha = \bigwedge_{i \in I_1} b_{1,i} \alpha_{1,i} \rightarrow \dots \rightarrow \bigwedge_{k \in I_k} b_{k,i} \alpha_{k,i} \rightarrow q.$$

Given an assignment function  $f$  for boolean variables, the type environment  $f(\Delta)$  is given by:

$$f(\Delta) = \{x_i : f(\rho_i) \mid x_i :^{b_i} \rho_i \in \Delta \wedge f(b_i) = \text{true}\} \\ f(E, \alpha) = (\{\alpha = f(\xi_1) \rightarrow \dots \rightarrow f(\xi_k) \rightarrow q \mid (\alpha = \xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q) \in E\}, \alpha) \\ f(\bigwedge_{i \in I} b_i \alpha_i) = \bigwedge \{\alpha_i \mid i \in I, f(b_i) = \text{true}\}$$

Let  $\Delta$  be the type environment obtained by attaching boolean variables to  $\Gamma^{(k)}$ . Then, the condition  $\Gamma' \sqsubseteq_{\mathcal{S}} \Gamma^{(k)} \wedge \Gamma' \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$  is reduced to: “Is there a boolean assignment  $f$  such that  $f(\Delta) \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B},0}$ ?” It can be expressed as a SAT problem as follows. We first introduce additional boolean variables: (i) For

each rule  $F \mapsto \lambda x_1 \dots \lambda x_k. t \in \mathcal{R}$ , a subterm  $s$  of  $t$ , a type binding  $F :^b \xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \in \Delta$ , and a type  $\rho$  in  $\Delta$ , we prepare a variable  $b_{\Delta, x_1 : \xi_1, \dots, x_k : \xi_k \vdash s : \rho}$ , which expresses whether  $f(\Delta, x_1 : \xi_1, \dots, x_k : \xi_k) \vdash_{\mathcal{B}} s : f(\rho)$  should hold. (ii) For each pair  $(\rho_1, \rho_2)$  of types occurring in  $\Delta$ , we introduce  $b_{\rho_1 \leq \rho_2}$ , which expresses whether  $f(\rho_1) \leq f(\rho_2)$  should hold. Now, the existence of a boolean assignment function  $f$  such that  $f(\Delta) \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B}, 0}$  is reduced to the satisfiability of the conjunction of all the following boolean formulas. We write  $F : \bigwedge_{j \in \{1..n\}} b_j \rho_j \in \Delta$  for  $F :^{b_1} \rho_1, \dots, F :^{b_n} \rho_n \in \Delta$  below. For simplicity, we omit type equations  $E$  and identify  $\alpha$  and  $E(\alpha)$  below.

- (i)  $\bigvee \{b_i \mid S :^{b_i} q_{\mathcal{B}, 0} \in f(\Delta)\}$ .
- (ii)  $b \Rightarrow b_{\Delta, x_1 : \xi_1, \dots, x_k : \xi_k \vdash t : q}$   
for each  $F :^b \xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \in \Delta$  such that  $\mathcal{R}(F) = \lambda x_1, \dots, x_k. t$ .
- (iii)  $b_{\Delta', x : \bigwedge_{j \in J} b_j \rho_j \vdash x : \rho} \Rightarrow \bigvee_{j \in J} (b_j \wedge b_{\rho_j \leq \rho})$ , for each  $b_{\Delta', x : \bigwedge_{j \in J} b_j \rho_j \vdash x : \rho}$ .
- (iv)  $b_{\Delta' \vdash a : \rho} \Rightarrow \bigvee \{b_{q_1 \rightarrow \dots \rightarrow q_k \rightarrow q \leq \rho} \mid \delta(a, q) = q_1 \dots q_k\}$ , for each  $b_{\Delta' \vdash a : \rho}$ .
- (v)  $b_{\Delta' \vdash t_1 t_2 : \rho} \Rightarrow \bigvee (b_{\Delta' \vdash t_1 : (\bigwedge_{j \in J} b_j \rho_j) \rightarrow \rho} \wedge (\bigwedge_{j \in J} (b_j \Rightarrow \bigvee (b_{\Delta' \vdash t_2 : \rho'} \wedge b_{\rho' \leq \rho_j}))))$ ,  
for each  $b_{\Delta' \vdash t_1 t_2 : \rho}$ .
- (vi)  $b_{(\bigwedge_{i \in I} b_i \rho_i) \leq (\bigwedge_{j \in J} b_j \rho'_j)} \Rightarrow \bigwedge_{j \in J} (b_j \Rightarrow \bigvee_{i \in I} (b_i \wedge b_{\rho_i \leq \rho'_j}))$ ,  
for each  $b_{(\bigwedge_{i \in I} b_i \rho_i) \leq (\bigwedge_{j \in J} b_j \rho'_j)}$ .
- (vii)  $b_{\xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \leq \xi'_1 \rightarrow \dots \rightarrow \xi'_m \rightarrow q'} \Rightarrow k = m \wedge q = q' \wedge \bigwedge_{i \in \{1, \dots, k\}} b_{\xi'_i \leq \xi_i}$ ,  
for each  $b_{\xi_1 \rightarrow \dots \rightarrow \xi_k \rightarrow q \leq \xi'_1 \rightarrow \dots \rightarrow \xi'_m \rightarrow q'}$ .

The first condition (i) ensures that  $S : q_{\mathcal{B}, 0} \in f(\Delta)$ . The condition (ii) ensures that each type binding in  $f(\Delta)$  is valid (i.e.,  $\vdash_{\mathcal{B}} \mathcal{R} : f(\Delta)$ ). The next three conditions (iii)-(v) express the validity of a type judgment  $f(\Delta, x_1 : \xi_1, \dots, x_k : \xi_k) \vdash_{\mathcal{B}} t : f(\rho)$ , corresponding to the typing rules for variables, constants, and applications. The last two conditions express the validity of a subtype relation.

By the above construction, there exists a boolean assignment function  $f$  such that  $f(\Delta) \vdash_{\mathcal{B}} (\mathcal{G}, S) : q_{\mathcal{B}, 0}$  if and only if the conjunction of the above boolean formulas is satisfiable. The latter can be solved by using a SAT solver.

*Example 11.* Recall  $\Gamma_{\mathcal{X}^{(2)}, \mathcal{U}^{(2)}, \sim^{(2)}}$  in Example 10. By adding it with boolean variables, we obtain  $\Delta = \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G :^{b_0} \tau_0, G :^{b_1} \tau_1\}$ , where  $\tau_i = (b_2 \tau_0 \wedge b_3 \tau_1) \rightarrow (q_0 \wedge q_1) \rightarrow q_i$  for  $i \in \{0, 1\}$ . (Here, for the sake of simplicity, we have added boolean variables only to critical parts.) From the typing of  $G$ , we get the following boolean constraints:

$$\begin{aligned} b_i &\Rightarrow b_{\Delta' \vdash a x (g g (b x)) : q_i} \quad (\text{for } i \in \{0, 1\}) & b_{\Delta' \vdash a x (g g (b x)) : q_0} &\Rightarrow b_{\Delta' \vdash g g : q_0 \wedge q_1 \rightarrow q_0} \\ b_{\Delta' \vdash a x (g g (b x)) : q_1} &\Rightarrow \mathbf{false} & b_{\Delta' \vdash g : \tau_0} &\Rightarrow b_2 & b_{\Delta' \vdash g : \tau_1} &\Rightarrow b_3 \\ b_{\Delta' \vdash g g : q_0 \wedge q_1 \rightarrow q_0} &\Rightarrow (b_{\Delta' \vdash g : \tau_0} \wedge (b_2 \Rightarrow b_{\Delta' \vdash g : \tau_0}) \wedge (b_3 \Rightarrow b_{\Delta' \vdash g : \tau_1})) \end{aligned}$$

Here,  $\Delta' = \Delta \cup \{g :^{b_2} \tau_0, g :^{b_3} \tau_1, x : q_0, x : q_1\}$ . The above conditions are satisfied by  $f$  such that  $f(b_0) = f(b_2) = \mathbf{true}$  and  $f(b_1) = f(b_3) = \mathbf{false}$ . Thus, we get  $\Gamma' = f(\Delta) = \{S : q_0, F : (q_0 \wedge q_1) \rightarrow q_0, G : \tau_0\}$  where  $\tau_0 = \tau_0 \rightarrow (q_0 \wedge q_1) \rightarrow q_0$ . We have  $\Gamma' \vdash_{\mathcal{B}_1} (\mathcal{G}_2, S) : q_0$  as required.  $\square$

## 4 Applications

This section discusses two applications of  $\mu$ HORS model checking: verification of (functional) object-oriented programs and that of higher-order multi-threaded programs. Those programs can be verified via reduction to  $\mu$ HORS model checking. In both applications, the translation from a source program to  $\mu$ HORS is just like giving the semantics of the source program (in terms of the  $\lambda$ -calculus). This comes from the expressive power of the model of  $\mu$ HORS model checking (i.e.,  $\mu$ HORS), which is the main advantage of our approach.

### 4.1 Model-Checking Functional Objects

In this section, we discuss how to reduce verification problems for (functional) object-oriented programs. The idea is to transform a program into  $\mu$ HORS that generates a tree representing all the possible action sequences<sup>3</sup> of the source program. The translation is sound *and complete* in the sense that all *and only* action sequences that occur are represented in the tree. Properties that can be checked include: reachability (i.e., whether program execution reaches certain program points), order of method invocations, and whether downcasts may fail. In a full version [10], we give a formal translation from (a call-by-value variant of) Featherweight Java (FJ) [5] to  $\mu$ HORS.

We use the following classes that represent natural numbers with methods for addition (`add`) and predecessors (`pred`) as a running example. The statement `fail`; signals a global action that denotes a failure. Method `rand` non-deterministically returns a natural number that is equal to or greater than the argument;  $\square$  denotes a non-deterministic choice operator. The main expression to be executed takes a predecessor of a (non-deterministically chosen) non-zero natural number.

```
class Nat extends Object {
  Nat add(Nat n) { fail; }
  Nat pred() { fail; }
  Nat rand(Nat n) { return n\square new S(this.rand(n)); } }
class Z extends Nat { Nat add(Nat n) { return n; } }
class S extends Nat {
  Nat p;
  Nat add(Nat n) { Nat p' = this.p.add(n); return new S(p'); }
  Nat pred() { return this.p; } }
// main expression
new Z().rand(new Z()).add(new S(new Z())).pred();
```

To verify program execution does not `fail`, we translate the program to a  $\mu$ HORS that generates the tree representing all the possible global events, like: `br e (br e ...)`. Here, `br` and `e` represent non-deterministic branch (caused by

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<sup>3</sup> The source language has a construct to signal a global action, as well as a non-deterministic choice operator.

□) and program termination, respectively. Then, it suffices to check that the tree does not contain `fail` by using  $\mu$ HORS model checking.

*Translation to  $\mu$ HORS.* The main ideas of translation are: (i) to express an object as a record (or tuple) of functions that represent methods [2], and (ii) to represent each method in the continuation passing style (CPS) in order to correctly reflect the evaluation order and action sequences to  $\mu$ HORS. For example, an object of class  $\mathbf{S}$  is expressed by a tuple  $\langle S\_add, S\_pred, S\_rand \rangle$  of functions  $S\_add$ ,  $S\_pred$  and  $S\_rand$  that represent methods `add`, `pred`, and `rand` defined or inherited in class  $\mathbf{S}$ , respectively.<sup>4</sup> A function that represents a method takes an argument that represents “self” and a continuation argument, as well as ordinary arguments of the method. In general, a method of the form

$$C_0 \ m(C_1 \ x_1, \dots, C_n \ x_n) \ \{ \text{return } e; \}$$

is represented by the  $\lambda$ -term  $\lambda x_1. \dots \lambda x_n. \lambda this. \lambda k. \llbracket e \rrbracket_k$  where  $k$  is the continuation parameter and  $\llbracket e \rrbracket_k$  denotes the translation of  $e$ , which passes the result of method execution to  $k$ . For example, method `add` in class  $\mathbf{S}$  is represented by non-terminal  $S\_add$ , whose body is

$$\lambda n. \lambda this. \lambda k. \llbracket \text{Nat } p' = \dots; \text{return new } \mathbf{S}(p') \rrbracket_k$$

Then, method invocation is expressed as self-application [7]. For example, invocation of `add` on an  $\mathbf{S}$  object with a  $\mathbf{Z}$  object as an argument is expressed by

$$S\_add \ \langle Z\_add, Z\_pred, Z\_rand \rangle \ \langle S\_add, S\_pred, S\_rand \rangle \ k$$

where  $k$  is the current continuation. Note that  $S\_add$  is applied to a tuple that contains itself.

To deal with fields, each method is further abstracted by values of fields of `this`. So, the body of  $S\_add$  is in fact

$$\lambda p. \lambda n. \lambda this. \lambda k. \llbracket \text{Nat } p' = \dots; \text{return new } \mathbf{S}(p') \rrbracket_k,$$

where  $p$  stands for `this.p` inside the method body. Although this scheme only supports field access of the form `this.f`, field access to any expressions other than `this` can be expressed by using “getter” methods. A non-terminal representing a method will be applied to initial field values when an object is instantiated. For example, object instantiation `new  $\mathbf{S}(p')$`  is represented by  $\langle S\_add \ p', S\_pred \ p', S\_rand \ p' \rangle$ . By using pattern-matching for  $\lambda$ , method `add` in class  $\mathbf{S}$  is expressed by the following two rules:

$$\begin{array}{l} S\_add \mapsto \lambda \langle p_a, p_p, p_r \rangle. \lambda \langle n_a, n_p, n_r \rangle. \lambda \langle this_a, this_p, this_r \rangle. \lambda k. \\ \quad p_a \ \langle n_a, n_p, n_r \rangle \ \langle p_a, p_p, p_r \rangle \ (F \ k), \\ F \quad \mapsto \lambda k'. \lambda \langle p'_a, p'_p, p'_r \rangle. \\ \quad k' \ \langle S\_add \ \langle p'_a, p'_p, p'_r \rangle, S\_pred \ \langle p'_a, p'_p, p'_r \rangle, S\_rand \ \langle p'_a, p'_p, p'_r \rangle \rangle \end{array}$$

<sup>4</sup>  $\mu$ HORS does not have tuples as primitives but all the tuples can be eliminated. Tuples as function arguments can be eliminated by currying and there is no function that returns tuples, thanks to the CPS representation. See the full version for details.

where  $F$  stands for the continuation of the variable definition  $\text{Nat } p' = \dots$ .

A global action  $a$  such as `fail` is represented by a tree node  $a$ ; non-deterministic choice is by the node `br` of arity 2. The (translation of the) main expression is given as the initial continuation a constant function that returns the tree node `e` of arity 0. So, in order to verify that the program does not `fail`, it suffices to verify that the generated tree consists only of nodes `br` and `e`.

We address the problem of the lack of subtyping in  $\mu\text{HORS}$  as follows. We represent every object as a tuple of the *same length*  $\ell$ , where  $\ell$  is the number of the methods defined in the whole program. If a certain method is undefined, we just insert a dummy function  $\lambda\tilde{x}.\lambda k.\text{fail}$  in the corresponding position of the tuple. The dummy function just outputs `fail` to signal `NoSuchMethodError` whenever it is called.

The resulting encoding of an object is well-typed. Let  $\{m_1, \dots, m_\ell\}$  be all the method names in the program, and  $\{n_1, \dots, n_\ell\}$  be their arities. Then, the encoding of every object would have the same recursive sort  $\kappa_o$ , given by:

$$\kappa_o = \kappa_{m_1} \times \dots \times \kappa_{m_\ell} \quad \kappa_{m_i} = \underbrace{\kappa_o \rightarrow \dots \rightarrow \kappa_o}_{n_i} \rightarrow \kappa_o \rightarrow \underbrace{(\kappa_o \rightarrow \circ)}_{\text{type of continuation}} \rightarrow \circ$$

The source program execution yields a sequence of global actions  $a_1 a_2 \dots a_n$  if and only if the tree generated by the translation has a path labeled with  $a_1 a_2 \dots a_n$  (ignoring `br`). Thus safety property verification of FJ programs is reduced to  $\mu\text{HORS}$  model checking.

## 4.2 Model-Checking Higher-Order Multi-Threaded Programs

This section discusses how to apply the extended HO model checking to verification of multi-threaded programs, where each thread may use higher-order functions and recursion. For the sake of simplicity, we discuss only programs consisting of two threads, whose syntax is given by:

$$\begin{aligned} P \text{ (programs)} &::= M_1 \parallel M_2 \\ M \text{ (threads)} &::= () \mid a \mid x \mid \text{fun}(f, x, M) \mid M_1 M_2 \mid M_1 \square M_2 \end{aligned}$$

A program  $P = M_1 \parallel M_2$  executes two threads  $M_1$  and  $M_2$  concurrently, where  $M_1$  and  $M_2$  are (call-by-value) higher-order functional programs with side effects. The expression  $a$  performs a global action  $a$ , and evaluates to the unit value  $()$ . We keep global actions abstract, so that various synchronization primitives and shared memory can be modeled. The expression  $\text{fun}(f, x, M)$  describes a recursive function  $f$  such that  $f(x) = M$ . When  $f$  does not occur in  $M$ , we write  $\lambda x.M$  for  $\text{fun}(f, x, M)$ . We also write  $\text{let } x = M_1 \text{ in } M_2$  for  $(\lambda x.M_2)M_1$ , and further abbreviate it to  $M_1; M_2$  when  $x$  does not occur in  $M_2$ .  $M_1 \square M_2$  evaluates  $M_1$  or  $M_2$  non-deterministically. The formal semantics is given in [10]. The goal of verification is, given a program  $M$  and a property  $\psi$  on global action sequences, to check whether all the possible action sequences of  $M$  satisfy  $\psi$ .

*Example 12.* Let  $M$  be the following thread:

**let**  $sync\ f = lock; f(); unlock; sync\ f$  **in let**  $cr\ x = enter; exit$  **in**  $sync\ cr,$

which models a thread acquiring a global lock before entering a critical section. We may then wish to verify that the global actions **enter** and **exit** can occur only alternately, as long as **lock** and **unlock** occur alternately.  $\square$

We can reduce verification problems for multi-threads to extended HO model checking problems by transforming a given program to a  $\mu$ HORS that generates a tree describing all the possible global action sequences. The ideas of the transformation are: (i) transform each thread to CPS (continuation-passing style) to correctly model the order of actions, as in [9], and (ii) apply each thread to a scheduler, and let a thread pass the control to the scheduler non-deterministically after each global action. The translation from programs to  $\mu$ HORS is:

$$\begin{aligned} (M_1 \parallel M_2)^\dagger &= \mathbf{br} (Sched (M_1^\dagger \lambda x.e) (M_2^\dagger \lambda x.e)) (Sched (M_2^\dagger \lambda x.e) (M_1^\dagger \lambda x.e)) \\ ()^\dagger &= \lambda k.\lambda g.k\ \mathbf{e}\ g \quad x^\dagger = \lambda k.\lambda g.k\ x\ g \quad \mathbf{fun}(f, x, M)^\dagger = \lambda k.\lambda g.k\ \mathbf{fun}(f, x, M^\dagger)\ g \\ (M_1 M_2)^\dagger &= \lambda k.\lambda g.M_1^\dagger (\lambda f.M_2^\dagger \lambda x.f\ x\ k)\ g \\ (M_1 \square M_2)^\dagger &= \lambda k.\lambda g.\mathbf{br} (M_1^\dagger k\ g) (M_2^\dagger k\ g) \quad a^\dagger = \lambda k.\lambda g.a\ (\mathbf{br} (k\ \mathbf{e}\ g) (g\ (k\ \mathbf{e}))) \end{aligned}$$

Here, the non-terminal  $Sched$  is defined by the rule  $Sched\ x\ y \rightarrow x (Sched\ y)$ , which schedules  $x$  first, passing to it the global continuation  $Sched\ y$  (which will schedule  $y$  next). The terminal symbol **br** represents a non-deterministic branch. On the righthand side of the last translation rule,  $a$  and **e** are terminal symbols of arity 1 and 0 respectively. The program  $M_1 \parallel M_2$  is translated to a tree-generating program, which either schedules  $M_1$  then  $M_2$ , or  $M_2$  then  $M_1$ . Apart from the global action (the last rule), the translation of a thread is essentially the standard call-by-value CPS transformation except that a global continuation is passed as an additional parameter. The global action  $a$  is transformed to a tree node  $a$ , followed by a non-deterministic branch (expressed by **br**). The first branch evaluates the local continuation, while the second branch yields the control to the other thread by invoking the global continuation  $g$ .

By the definition of the transformation above, it should be clear that (i) if  $P$  is simply-typed, then  $P^\dagger$  is a well-typed  $\mu$ HORS, and (ii)  $P$  has a sequence of global actions  $a_1 a_2 \cdots a_n$  if and only if the tree generated by  $P^\dagger$  has a path labeled with  $a_1 a_2 \cdots a_n$  (with **br** ignored). Thus, verification of multi-threaded programs has been reduced to  $\mu$ HORS model checking; see [10] for more details.

*Context-bounded model checking* Qadeer and Rehof [19] showed that model checking of concurrent pushdown systems (or multi-threaded programs with *first-order* recursion) is decidable *if the number of context switches is bounded by a constant*. Our translation given above yields a generalization of the result: context-bounded model checking of multi-threaded, *higher-order* recursive programs is decidable. To obtain the result, it suffices to replace the scheduler  $Sched$  with  $Sched_\ell$  given below, which allows only  $\ell$  context switches:

$$Sched_0\ x\ y \rightarrow \mathbf{e} \quad Sched_{i+1}\ x\ y \rightarrow x (Sched_i\ y)$$

Then  $Sched_i$ 's have the following *non-recursive* types:

$$Sched_{2m} : \sigma_m \rightarrow \sigma_m \rightarrow \circ \quad Sched_{2m+1} : \sigma_{m+1} \rightarrow \sigma_m \rightarrow \circ$$

where  $\sigma_0 = \top$  and  $\sigma_{i+1} = (\sigma_i \rightarrow \circ) \rightarrow \circ$ . Thus, for the modified encoding  $P^{\dagger\ell}$ , we have: (i) If  $P$  is simply-typed, then  $P^{\dagger\ell}$  is a well-typed  $\mu$ HORS *without recursive types*, and (ii)  $P$  with context-bound  $\ell$  has a sequence of global actions  $a_1 a_2 \cdots a_n$  if and only if the tree generated by  $P^{\dagger\ell}$  has a path labeled with  $a_1 a_2 \cdots a_n$  (with `br` ignored). As an immediate corollary of the above properties and the decidability of HORS model checking [15], we obtain that context-bounded model checking of multi-threaded higher-order programs is decidable.<sup>5</sup>

## 5 Implementation and Experiments

We have implemented a prototype model checker RTRECS for  $\mu$ HORS based on the procedure FINDCERT described in Section 3. As the underlying SAT solver, we have used MiniSat 2.2 (<http://minisat.se/MiniSat.html>). We have also implemented a translator from FJ programs to  $\mu$ HORS based on Section 4.1.

The implementation is based on the procedure FINDCERT in Figure 2, except for the following points. RTRECS first performs an equality-based flow analysis [17] before model checking, and uses it as the equivalence relation  $\sim$ .  $\mathcal{U}$  is also over-approximated by using the result of the flow analysis; thus, in the current implementation, the reductions of terms (3rd line in Figure 2) are performed only for finding a counter-example, without using the rule R-EQ.

Table 1 summarizes the result of preliminary experiments. (For space restriction, we omit some results, which are found in [10].) The programs used for the experiments and the web interface for our prototype are available at <http://www-kb.is.s.u-tokyo.ac.jp/~koba/fjmc/>. The columns “#lines” and “#rules” show the number of lines of the source FJ program (if applicable) and the number of the rules of  $\mu$ HORS. The column “#states” shows the number of states of the property automaton. The column “ $k$ ” shows  $k$  of  $\Gamma' \sqsubseteq_s \Gamma^{(k)}$  in Section 3.2. The column “#sat” shows the number of sat clauses (i.e., the number of disjunctive formulas in conjunctive normal form) for the final call of the SAT solver. The column “time” shows the running time (excluding the time for translation from FJ to  $\mu$ HORS, which is anyway quite small).

$\mathcal{G}_1$  and  $\mathcal{G}_2$  are from Examples 2 and 3, where the checked property is expressed by  $\mathcal{B}_1$  in Example 4. `Thread` is the  $\mu$ HORS obtained from Example 12. The other programs were obtained from FJ programs, based on the translation discussed in Section 4.1, and except for `Twofiles`, we verified that the programs do not fail (where the meaning of “failure” depends on each program, as explained below). `Pred` is the running example in Section 4. The next six are list-manipulating programs (implemented as objects), which are small but

<sup>5</sup> We have considered only programs with two threads, but this restriction can be easily relaxed by using the same technique as Qadeer and Rehof [19].

programs	#lines	#rules	#states	$k$	#sat	time
$\mathcal{G}_1$	–	2	2	1	27	0.001
$\mathcal{G}_2$	–	3	2	1	49	0.002
Thread	–	9	5	1	38,171	0.580
Pred	21	15	1	1	157	0.005
L-append	20	30	1	1	165	0.006
L-map	43	182	1	1	738	0.235
L-app-map	43	212	1	1	1,546	0.391
L-even	25	87	1	1	249	0.025
L-filter	59	122	1	2	5,964	0.491
L-risers	73	64	1	2	17,419	0.445
Twofiles	28	21	5	2	739,867	13.86

**Table 1.** Experimental Results (CPU: Intel(R) Xeon(R) 3GHz, Memory: 8GB). Times are in seconds.

non-trivial programs. (In fact, **L-filter** and **L-risers** are object-oriented versions of benchmark programs of the PMRS verification tool [16].) For example, **L-filter** creates a list of natural numbers in a non-deterministic manner, filters out 0, and checks that the resulting list consists only of non-zero elements (and fails if it does not hold). See [10] for more details.

**Twofiles** was prepared as an example of verification of temporal properties. It is an object-oriented version of the program that accesses two files: one for read-only, and the other for write-only [9]. We verify that the read-only (write-only, resp.) file is closed after some reads (writes, resp.).

Our model checker RTRECS could successfully verify all the programs. The verification time and the size of SAT formulas were significantly larger for **Twofiles** compared with other programs. The explosion of the size of SAT formulas for **Twofiles** is due to the size of the automaton for describing the temporal property, which blows up the number of candidates of types to be considered. More optimizations are necessary for avoiding this problem. The number  $k$  was surprisingly small for all the benchmark programs; this indicates that our choice of  $\sim$  based on the equality-based flow analysis provided a good approximation of types. Overall, the experimental results above are encouraging; we are not aware of other fully-automated (i.e. requiring no annotations), sound (i.e. no false negatives) verification tools that can verify all the programs above.

## 6 Related Work

The model checking of HORS has recently emerged as a new technique for verification of higher-order programs [15, 9, 14, 16, 13]. Except Tsukada and Kobayashi’s work [24], however, all the previous studies dealt with *simply-typed* recursion schemes, which are not suitable for modeling objects. Tsukada and Kobayashi [24] studied model checking of *untyped* HORS and reduced it to a type checking

problem for an infinite intersection type system. The latter problem is however undecidable and they did not provide any realistic procedure for model checking.

Several methods for model-checking functional programs have been proposed recently [21, 9, 14, 16, 25], and some of them [21, 14, 16] support recursive data structures (like lists). However, it is not clear how to extend them to support general recursive types (including negative occurrences of recursive type variables). Furthermore, many of them require annotations [21, 25] and are less precise.

There are previous studies on model checking of object-oriented programs [3, 4]. To our knowledge, however, they are based on finite state model checking; Java programs are either (i) abstracted to finite state models and then finite state model checkers are used to verify the abstract models, or (ii) directly model checked, but with an incomplete state exploration. In the former case, because of the huge semantic gap between object-oriented programs and finite state systems, a lot of information is lost by the translation from Java programs to models. In the latter case, a “model checker” is used mainly as a bug detection tool, instead of a verification tool. In contrast, our method uses  $\mu$ HORS as models, which are as expressive as source programs. No information is lost by the translation from FJ to  $\mu$ HORS, and no false alarms can be generated (although the model checker may not terminate for some valid programs). There are also other methods for verification or static analysis of object-oriented programs [1, 18, 23]. In general, they either require human intervention [1] or are fully automated but less precise than model checking. See [10] for more detailed discussion. Rowe and Bakel [22] proposed an intersection type system for reasoning about object-oriented programs, but did not give an automated verification algorithm.

There are many studies on model checking of recursive parallel programs [19, 6], which obtain decidable fragments by restricting synchronization primitives or applying approximations. It is interesting to see whether each result can be extended to higher-order, recursive parallel programs (besides context-bounded model checking discussed in Section 4.2).

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## References

1. M. Barnett, R. DeLine, M. Fähndrich, B. Jacobs, K. R. M. Leino, W. Schulte, and H. Venter. The spec# programming system: Challenges and directions. In *Proceedings of VSTTE 2005*, volume 4171 of *LNCS*, pages 144–152. Springer-Verlag, 2005.
2. L. Cardelli. A semantics of multiple inheritance. *Info. Comput.*, 76(2/3):138–164, 1988.
3. J. C. Corbett, M. B. Dwyer, J. Hatcliff, S. Laubach, C. S. Pasareanu, Robby, and H. Zheng. Bandera: extracting finite-state models from Java source code. In *ICSE*, pages 439–448, 2000.
4. K. Havelund and T. Pressburger. Model checking JAVA programs using JAVA pathfinder. *STTT*, 2(4):366–381, 2000.

5. A. Igarashi, B. C. Pierce, and P. Wadler. Featherweight Java: a minimal core calculus for Java and GJ. *ACM Trans. Prog. Lang. Syst.*, 23(3):396–450, 2001.
6. V. Kahlon. Reasoning about threads with bounded lock chains. In *Proceedings of CONCUR 2011*, volume 6901 of *LNCS*, pages 450–465. Springer-Verlag, 2011.
7. S. N. Kamin and U. S. Reddy. Two semantic models of object-oriented languages. In C. A. Gunter and J. C. Mitchell, editors, *Theoretical Aspects of Object-Oriented Programming*, chapter 13, pages 463–496. The MIT Press, 1993.
8. N. Kobayashi. Model-checking higher-order functions. In *Proceedings of PPDP 2009*, pages 25–36. ACM Press, 2009.
9. N. Kobayashi. Types and higher-order recursion schemes for verification of higher-order programs. In *Proc. of POPL*, pages 416–428, 2009.
10. N. Kobayashi and A. Igarashi. Model-checking higher-order programs with recursive types. An extended version available from <http://www-kb.is.s.u-tokyo.ac.jp/~koba/fjmc/>, 2012.
11. N. Kobayashi and C.-H. L. Ong. A type system equivalent to the modal mu-calculus model checking of higher-order recursion schemes. In *Proceedings of LICS 2009*, pages 179–188, 2009.
12. N. Kobayashi and C.-H. L. Ong. Complexity of model checking recursion schemes for fragments of the modal mu-calculus. *Logical Methods in Computer Science*, 7(4), 2011.
13. N. Kobayashi, R. Sato, and H. Unno. Predicate abstraction and cegar for higher-order model checking. In *Proc. of PLDI*, 2011.
14. N. Kobayashi, N. Tabuchi, and H. Unno. Higher-order multi-parameter tree transducers and recursion schemes for program verification. In *Proc. of POPL*, pages 495–508, 2010.
15. C.-H. L. Ong. On model-checking trees generated by higher-order recursion schemes. In *LICS 2006*, pages 81–90, 2006.
16. C.-H. L. Ong and S. Ramsay. Verifying higher-order programs with pattern-matching algebraic data types. In *Proc. of POPL*, pages 587–598, 2011.
17. J. Palsberg. Equality-based flow analysis versus recursive types. *ACM Trans. Prog. Lang. Syst.*, 20(6):1251–1264, 1998.
18. M. J. Parkinson and G. M. Bierman. Separation logic, abstraction and inheritance. In *Proc. of POPL*, pages 75–86, 2008.
19. S. Qadeer and J. Rehof. Context-bounded model checking of concurrent software. In *Proceedings of TACAS 2005*, volume 3440 of *LNCS*, pages 93–107. Springer-Verlag, 2005.
20. G. Ramalingam. Context-sensitive synchronization-sensitive analysis is undecidable. *ACM Trans. Prog. Lang. Syst.*, 22(2):416–430, 2000.
21. P. M. Rondon, M. Kawaguchi, and R. Jhala. Liquid types. In *PLDI 2008*, pages 159–169, 2008.
22. R. N. S. Rowe and S. van Bakel. Approximation semantics and expressive predicate assignment for object-oriented programming - (extended abstract). In *Proceedings of TLCA 2011*, volume 6690 of *LNCS*, pages 229–244, 2011.
23. C. Skalka. Types and trace effects for object orientation. *Higher-Order and Symbolic Computation*, 21(3):239–282, 2008.
24. T. Tsukada and N. Kobayashi. Untyped recursion schemes and infinite intersection types. In *Proceedings of FOSSACS 2010*, volume 6014 of *LNCS*, pages 343–357. Springer-Verlag, 2010.
25. H. Unno, N. Tabuchi, and N. Kobayashi. Verification of tree-processing programs via higher-order model checking. In *Proceedings of APLAS 2010*, volume 6461 of *LNCS*, pages 312–327. Springer-Verlag, 2010.